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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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Serial No.: 10/748,559

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For: A CMOS DEVICE WITH
METAL AND SILICIDE GATE
ELECTRODES AND A
METHOD FOR MAKING IT

Art Unit: unknown

Examiner: unknown

Attorney Docket: P18244

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with a copy of the reference cited on that form. It is respectfully requested that the cited reference be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement constitutes prior art or is otherwise material to patentability.

Respectfully submitted,

Dated: May 26, 2004

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(37 C.F.R. § 1.8(a))

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May 25, 2004

Date of Deposit

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May 25, 2004
Date

[illegible]

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ² Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature		Date Considered	
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